

Marvell® Alaska® 88E1545

Quad Port 10/100/1000 Mbps Energy Efficient Ethernet Transceivers

Overview

Marvell® Alaska® 88E1545 Gigabit Ethernet Transceiver is a physical layer device containing four independent Gigabit Ethernet transceivers. Each transceiver performs all the physical layer functions for 1000BASE-T, 100BASE-TX and 10BASE-T standards. The device supports Quad-Serial Gigabit Media Interface (QSGMII) to Copper.

In addition to supporting Energy Efficient Ethernet (EEE) on the new generation of enabled MACs, these products are also capable of implementing EEE with legacy or non-EEE devices by incorporating EEE buffering. The device also integrates MDI interface termination resistors into the PHY. This resistor integration simplifies board layout and reduces board cost by reducing the number of external components. The new Marvell calibrated resistor scheme will achieve and exceed the accuracy requirements of the IEEE 802.3 return loss specifications.

The Alaska 88E1545 device has an integrated switching voltage regulator to generate all required voltages and can run off a single 3.3V supply. This device uses advanced mixed-signal processing to perform equalization, echo and crosstalk cancellation, data recovery and error correction at a gigabit-per-second data rate. The device achieves robust performance in noisy environments with very low power dissipation.

The Marvell Alaska family of transceiver products provides the ideal solution for rapid development and deployment of gigabit standalone and switching systems for the Enterprise, embedded, consumer and Metro/service provider market segments.

Block Diagram

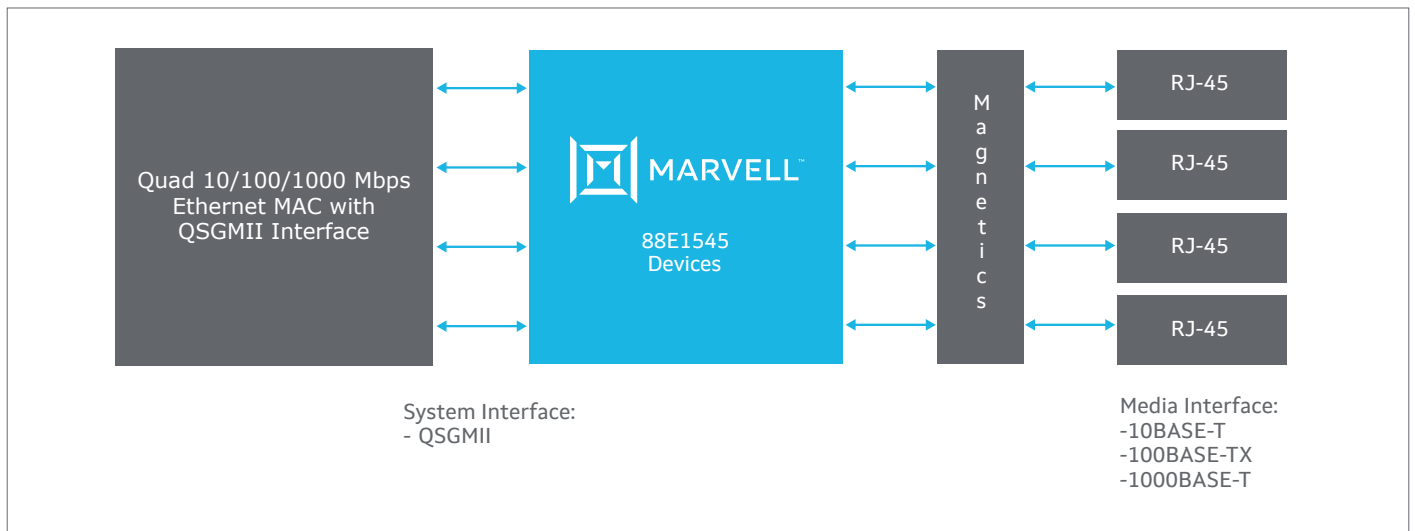


Figure 1. QSGMII to Copper Application

Key Features

Features	Benefits
<ul style="list-style-type: none">• Supports EEE (IEEE 802.3az)<ul style="list-style-type: none">– Implements EEE with legacy or non-EEE MAC	<ul style="list-style-type: none">• Extended energy savings through incorporation of the IEEE 802.3az standard<ul style="list-style-type: none">– Additional support added to allow EEE enablement on non-EEE MACs
<ul style="list-style-type: none">• Supports QSGMII MAC Interface	<ul style="list-style-type: none">• Lower pin count<ul style="list-style-type: none">– Simplifies board layout
<ul style="list-style-type: none">• Wake on LAN (WoL)	<ul style="list-style-type: none">• Provides programmable lower power (S5) event/pattern and link change detection
<ul style="list-style-type: none">• Integrated Switching Voltage Regulator	<ul style="list-style-type: none">• Allows devices to run off single 3.3V supply
<ul style="list-style-type: none">• Advanced Virtual Cable Tester® (VCT™)	<ul style="list-style-type: none">• Detects and reports potential cabling issues to within one meter of the distance to the fault
<ul style="list-style-type: none">• 14mm x 20mm 128-pin LQFP with EPAD	<ul style="list-style-type: none">• Green, environmentally friendly, small form factor for minimal real estate requirements with efficient heat dissipation

Target Applications

The Alaska 88E1545 Transceiver delivers optimal physical layer interfacing and features for a broad range of applications within the Enterprise, embedded, consumer and Metro/service provider market segments.

The Alaska 88E1545 family provides a complete GbE transceiver solution with complete software compatibility. To shorten system manufacturers' design cycles and accelerate time-to-market, complete Marvell Alaska reference designs and supporting docs with schematics, layout files and other documentation are provided.



To deliver the data infrastructure technology that connects the world, we're building solutions on the most powerful foundation: our partnerships with our customers. Trusted by the world's leading technology companies for 25 years, we move, store, process and secure the world's data with semiconductor solutions designed for our customers' current needs and future ambitions. Through a process of deep collaboration and transparency, we're ultimately changing the way tomorrow's enterprise, cloud, automotive, and carrier architectures transform—for the better.

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