

Marvell Unveils 802.3ch 10G Automotive Multi-Gigabit Ethernet PHY

Overview

Company founded

1995

\$3.0B

FY21 revenue

Employees

5,000+

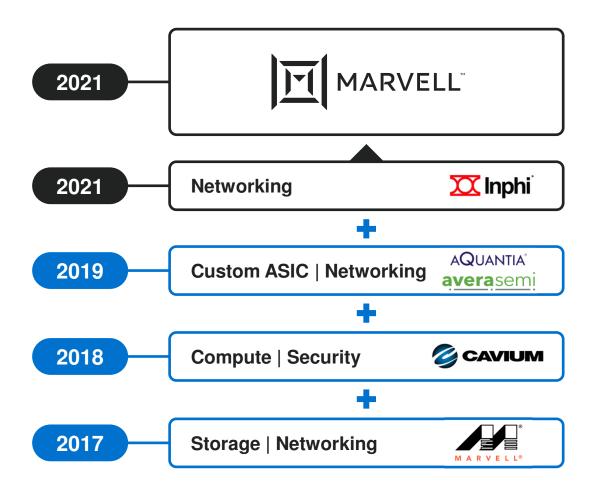
Patents worldwide

10,000+

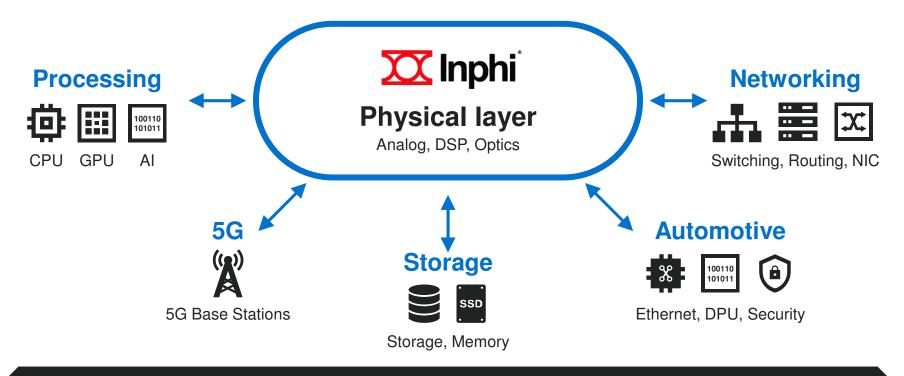
Located in **Santa Clara, CA**R&D centers in **US, Israel, India, Germany, China**



Transformation to premier data infrastructure partner



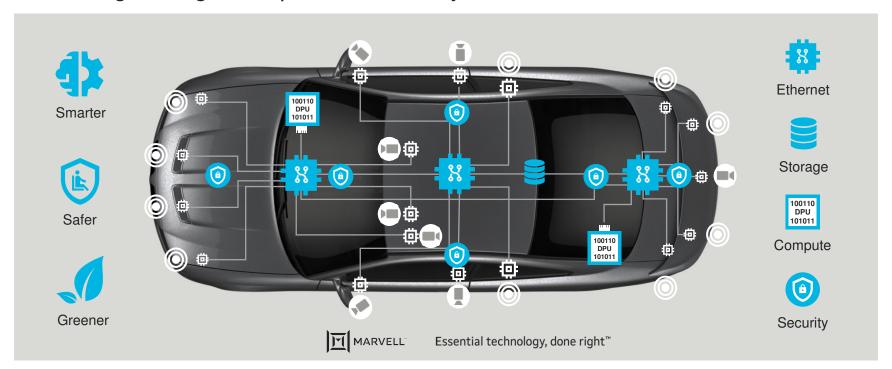
Inphi acquisition will add leading high-speed data interconnects



25-800Gbps+ electro-optics interconnect

Automotive Vision

Enabling smarter, safer and greener vehicles with innovative automotive networking, storage, compute and security semiconductor solutions.



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Marvell Automotive

Market leader for In-Vehicle-Network (IVN)



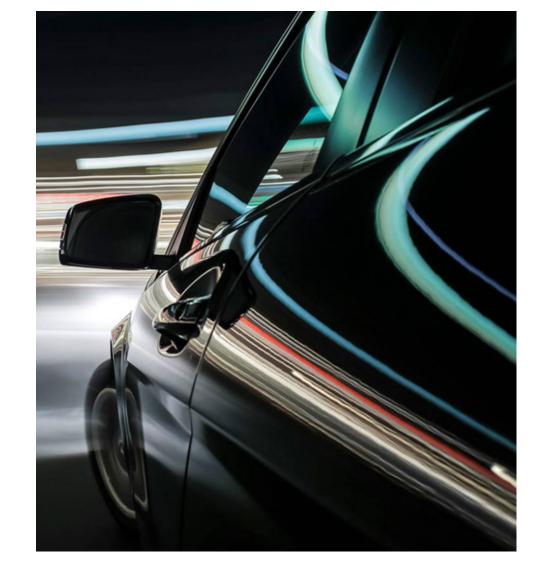
1,000+ engineers developing Ethernet IP and products



2,000+ Ethernet patents



30+ Automotive IVN products in 2020



Leader in automotive Ethernet technology

- 1st to market with 1000BASE-T1 PHY
- 1st to market with automotive secured switch
- 1st to market with multi-gig Ethernet PHY
- Complete roadmap: Switch, PHY, controller
- ASPICE Level 2 certification
- ASIL-B certification



Committed to investing in continuous improvement



Opened the Automotive Center of Excellence (ACE) in Ettlingen, Germany in 2017



Launched the first CISPR 25 qualified automotive EMC lab in North America in 2018



Achieved ASPICE Level II and ASIL-B certifications in 2019 to ensure quality software and robust products







Next generation automotive megatrends







Autonomous

Higher level of Autonomy driving high resolution sensor integration

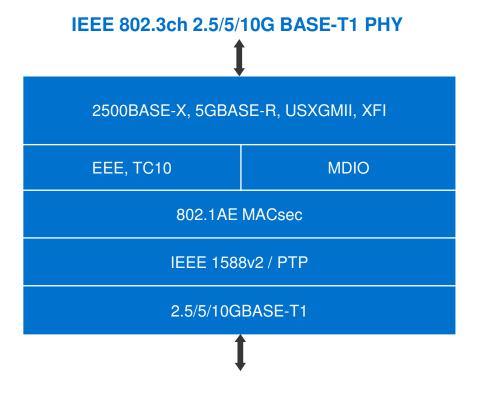
Connected

5G is enabling real time vehicle to external world communication for better safety and user experience

Software-Defined

Future cars are getting architected as a software defined. Car compute is getting centralized, requiring high speed networks

Introducing Marvell's 2nd generation 88Q4364 Ethernet PHY



Key Features

- IEEE 802.3ch compliant
- Speed Grades: 2.5/5/10GBASE-T1
- 802.1AE MACsec supported
- Energy Efficient Ethernet
- Sleep/Wake: TC10
- AEC-Q100 Grade 2 (-40 °C to +105 °C)
- 7x11 BGA

Secure multi-gig automotive Ethernet PHY



802.3ch Compliant Multi-gig Ethernet PHY (2.5G/5G/10G)



Layer 2 security through integrated MACsec



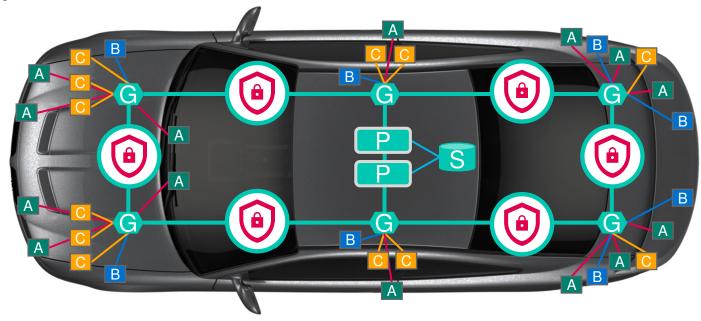
TC10 for Partial Networking



Asymmetric Data Transfer using Energy Efficient Ethernet

MACsec Integrated Automotive Multigig T1 PHY

- MACsec secures IVN by securing data exchange on a hop-by-hop basis
- Prevents Layer 2 security threats such as intrusion, man-in-the-middle, and replay attacks.



Low power through TC10 and EEE



TC10 Capable Multigig PHY

Enables Partial Networking allowing fast and on-demand wake / sleep for segments of IVN



Energy Efficient Ethernet

Power savings through asymmetric data transfer capability

Marvell extends the IVN leadership with 88Q4364

Industry's 1st automotive gigabit **Ethernet PHY**

Marvell's 1st automotive 100Base-T1 PHY







Industry's 1st automotive multi-gigabit **Ethernet PHY**



Marvell's 2nd Marvell's 2nd gen gigabit automotive PHY

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MARVELL

Q212x

gen automotive gen gigabit 100Base-T1 PHY

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Marvell's 3rd automotive PHY



MARVELL

Marvell's

2nd gen



2005 2011 2015

2016

2017

2018

2019

2020







First Wi-Fi module in a production vehicle



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Industry's first secure automotive switch



Secure multi-gigabit high port count automotive switch

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MARVELL





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MARVELL Q5040



Sampling now





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MARVELL Essential technology, done right™

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"The DRIVE Orin platform can scale from level 2 to level 5 autonomy by aggregating multiple ECUs over a high-speed network. Leveraging Marvell's latest 10Gbps Ethernet PHY, NVIDIA can provide high-speed connectivity at multi-gig speeds to enable load balancing with low latency, delivering enormous performance capability."

- Nvidia

Summary

1 Marvell extends leadership in automotive Ethernet with 2nd gen mGig PHY

2 MACsec integrated T1 PHY for Layer 2 security

3 Supports low power operation through TC10 and EEE

4 Triple speed PHY that supports all (2.5/5/10G) 802.3ch speed modes



Essential technology, done right[™]